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- □□□ 2
- □□□ web book agent 2026-09-18
- □□□□□ CZ-697D 2026-09-18
- □□□□□□ □□□□□□□□ 2026-09-18
- ITX68000L□□□□□□□□□□□□□□
- □□□ ITX68000L □□□ 2026-09-18
- ITX68000L□□□□□□□□ □□□□□□□□ LED□□□
- □□□□□□□□□□ ITX68000L□□□□□□□□□□ LED□□□□□

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URL: https://zinichi.net/home/cz-697d2_00/
 : CZ-697D
 : 3D /DIY
 : 2025.02.01

CZ-697D CZ-617D CZ-
697D

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CZ-697D CZ-617D
CZ-697D
1/2
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X68000 1/2
Z
Z

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CZ-697D

?????????????LED

XXXXXXXXXXXXXXXXXXXXXXXXXXXX LEDXXXXXXXXXXXX

XXXX 15KHzXXXXXXXXXXXXXXXXXXXX OFF
XXXXXXXXXXXXXXXXXXXX LEDXXXXXXXXXXXX

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XXXXXXXXXXXXXXXXXXXXXXXXXXXX LEDXXXX

??CZ??????????

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XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXXXXX XXXXXXX USB-TypeC
XXXXXXXXXXXX 5VXXXXXXXXXXXXXXXXXXXX

#CZXXXX XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXX CZ

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XX @XXXX (@zinfyk) January 18, 2025 XXXXX

CZXXXXXXXXXXXX XXXXXXXXXXXXXXX TypeCXXXXXXXXXXXX

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pic.twitter.com/gCgRWulqsJ — XX @XXXX (@zinfyk) January 12, 2025

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XXXXXXXXXXXX CZXXXXXXXXXXXXXXXXXXXX USB-TypeCXXXX

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web_book_agent 2026-09-18

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■■■	2026-09-18

 CZ-697D  CZ-617D
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CZ-697D CZ CZ-617D


 CZ-697D

X68000  1/2





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CZ-697D

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KeplerX[ ] @kunikicho
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☐ LED ☐

15KHz ☐ OFF ☐



































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XXXXXXXX USB-TypeCXXXXXXXX 5VXXXXXXXXXXXXXXXXXXXX

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項目	仕様	備考
材質	XXXXXXXXXXXXXXXXXXXX (樹脂) XXXXXXXXXXXXXXXXXXXX (樹脂) XXXXXXXXXXXXXXXXXXXX	XXXXXXXXXXXXXXXXXXXX 樹脂
色	XXXX LED XXXXXXXXXXXXXXXXXXXX PH XXXXXXXXXXXXXXXXXXXX XXXX (M2-6 2XXXX 2)	CZ XXXXXXXXXXXXXXXXXXXX XXXXXXXXXXXX
寸法	XXXXXXXXXXXXXXXXXXXX	CZ-697D CZ-617DXXXXXX CZ XXXXXXXXXXXX
CZXXXX	XXXXXXXXXXXXXXXXXXXX (6)XXXXXX (M4-10 2 M3-15XXXX 4)	CZ-697D XXXXXXXXXXXXXXXXXXXX XXXX

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XXXXXXXXXXXXXXXXXXXXXXXXXXXX 2025 1XXXXXX CZ-697D

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CZ-697D

4

[illegible]

The diagram shows a horizontal bus of 100 bits, represented by a long row of small squares. Above the bus, there are two groups of bits: the first 10 bits are grouped together, and the next 10 bits are grouped together. To the right of the bus, there are two LED indicators. The first LED is labeled 'LED' and is positioned above the 10th bit. The second LED is also labeled 'LED' and is positioned above the 90th bit.

[illegible]

CZ   CZ-697D



 USB-

TypeC 

The diagram shows three horizontal bars, each composed of small squares. The top bar is the longest, followed by the middle bar, and the bottom bar is the shortest. These bars represent the relative lengths of the three longest rivers in the world.

CZ-697D

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LEDXXXXXXXXXX

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CZXXXX

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CZXXXXXXXXXXXXXXXXXX

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4XXXXXXXXXXXXXXXXXX

CZXXXXXXXXXXXX

ITX68000L????????????????

ITX68000L????????????????

LED????

X68000

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X68000????

1987????X68000

????65,536????FM

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????CG

????X68000

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????X68000????

ITX68000L???

ITX68000L????MiniITX????PC????X68000

????RTX 4070 Ti Super????

140??325??295mm????SFX-L??2.5??SSD2

????ABS????

??HDD????X68000??ON/OFF

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????ITX68000L????

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X68000????ITX68000L????2??

1. ?????

2. ?????LED

????X68000????2????

XEiJ+t ? XM6t

$X_{Eij+t} \square XM6t \square \square \square \square X68000$

[illegible]

X68000

LED

[illegible]

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Windows [|||||]

X68000

LED

SNS

[illegible][illegible]

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USB

A horizontal number line with 20 equal intervals, labeled from 0 to 20.

[illegible]

LED

WINDOWS → X68000

→ USB

→ LED

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[illegible]

LEDTTTTT

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USB

ITX68000L

[illegible][illegible]

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- PCB 
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[illegible]

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LED

[illegible]

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KiCad

[illegible]

□□□□□□□□ KiCad□□ PCB□□□□□□□□□□□□□□□□

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LED

CAD

[illegible]

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XM6t

[illegible]

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[illegible]

ITX68000L

LED□□□□□

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[illegible]

The diagram illustrates the layout of a 100-pin MiniITX board. The board is represented by a grid of 100 pins. Various components are labeled: X68000 MiniITX, ITX68000L, LED, Windows, X68000, LED, PC, and a small square component.

The diagram illustrates the memory layout of an X68000 system. It shows several memory segments represented by horizontal bars:

- A segment labeled "X68000" and "1987" at the top left.
- A segment labeled "6 5536" and "FM" in the middle, indicating a 65536-byte segment.
- A segment labeled "X68000" at the bottom right.

Figure 1: Schematic representation of the experimental setup. The diagram shows a top-down view of a rectangular arena with a central circular platform. Various components are labeled: ITX68000L, X68000, MiniITX, PC, MiniITX, 2.5, 270mm, ITX68000L, and 2. The arena is divided into sections by dashed lines, and the central platform is marked with a circle.

ITX68000L ☐ HDD ☐ X68000 ☐ ON/OFF

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☐ X68000 ☐ ITX68000L ☐ 2 ☐ 1

☐ 1

☐ LED ☐ 2

☐

????????????????

Windows X68000

LED

SNS

XEij+t XM6t X68000

?????

USB

PC

LED

Windows → X68000

→ USB

→ LED

LED

USB

LED

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USB

ITX68000L

PCB

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LED

1

LED

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Kicad????????

PCB????????????

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ITX68000L????

LED

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CAD

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ITX68000L?????????? ?????????????LED???

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X ITX68000LXXXX
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?????X68000?????

X68000XXXXXXXXXXXX 1987
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?????ITX68000L

ITX68000L MiniITX PC X68000 PC

140 325 295mm GeForce RTX4070Ti Super X68000 3D

ITX68000L ITX68000L ITX68000L

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ITX68000L HDD X68000 ON/OFF

HDD X68000 PC

HDD

X68000?????ITX68000L?????

X68000 ITX68000L 2

LED

X68000 5.25 2 LED LED

ITX68000L PC X68000

X68000 FDD LED

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LED

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?? Windows??????? X68000????????????????

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???? SNS?? ITX68000L??????? LED

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PC

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LED????????????

WINDOWS → X68000????

→ USB????

→ LED

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USB????

LED

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LED????

X68000

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ITX68000LXXXXXXXXXXXX

USBXXXX

XXXXXX USBXXXXXXXXXXXX PCXXXX

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XXXXXXXX USBXXXXXXXXXXXX ITX68000L
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PCBXXXXXXXXXX
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ITX68000L????

LEDXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX

CAD
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CAD

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3D

XXXXXXXX CADXXXXXXXXXXXXXXXXXXXX

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XM6t
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ITX68000LXXXXXXXXXX

LEDXXXXXXXXXX

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ITX68000L  LED  X68000



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- ITX68000L
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- ITX68000L
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ITX68000L  MiniITX  PC  X68000 



 HDD  X68000  ON/OFF 





ITX68000L HDD

Diagram illustrating the pinout of the ITX68000L chip, showing connections to X68000, LED, and ITX68000L.

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XEij+t XM6t X68000

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LEDXXXXXXXXXXXX

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LED ITX68000L

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Windows X68000XXXXXXXXXXXXXXXXXXXXXXXXXXXX

LED

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SNS

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XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX

USB

XXXXXXXXXXXXXXXXXXXX

PC LED

WINDOWS → X68000 → USB → LED

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LED

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USBXXXXXXXXXXXXXXXXXXXX

LED

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XXXXXXXXXXXXXXXXXXXX

USBXXXXXXXXXXXXXXXXXXXX

ITX68000L

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XXXXXXXXXXXX

PCBXXXXXXXXXXXX

XXXXXXXXXXXX



XM6t



LED

[illegible][illegible]

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